

## **REMARKS**

With this Response, Applicants respectfully request that claims 48, 50, 53, and 58 be canceled without prejudice. Additionally, claims 44, 47, 49, 51, and 56 are amended, and claims 61-64 are presented herein. Therefore, claims 44-47, 49, 51-52, 54-57, and 59-64 are pending.

## **ALLOWABLE SUBJECT MATTER**

Claims 46, 49-50, 53, and 58 were objected to as being dependent upon rejected base claims, but would be allowable if rewritten in independent form. Applicants have elected to incorporate the limitations of claims 48 and 50 into claim 44, the limitations of claim 53 into claim 51, and the limitations of claim 58 into claim 56, to expedite prosecution of the case to allowance. Applicants thus respectfully submit that independent claims 44, 51, and 56 are allowable as written. Claims 48, 50, 53, and 58 are canceled herein, rendering allowance of these claims moot.

## **CLAIM REJECTIONS - 35 U.S.C. § 102**

Claims 44, 47-48, 51, 54-56, and 59-60 were rejected under 35 U.S.C. § 102(b) as being anticipated by Japanese Patent Publication No. 05-120128 of Maruyama Kazuhiko et al (hereinafter *Maruyama*). As per above, Applicants have included limitations of claims found to be allowable over this reference into the independent claims. Thus, Applicants respectfully submit that these claims are allowable over the cited reference as currently written, rendering rejection of these claims moot.

## **CLAIM REJECTIONS - 35 U.S.C. § 103**

Claim 45 was rejected under 35 U.S.C. § 103(a) as being unpatentable over *Maruyama* in view of the Official Notice taken at page 5 of the Office Action that "it is well known in the art

to store memory devices (such as the local memory in Maruyama) in a memory card slot on a printed circuit board to interface to the system bus." As a matter of preserving Applicants' right to argue the issue at a later date, if at some time it becomes applicable, Applicants first state that they are unable to understand this rejection. There is nothing in the Office Action that indicates to Applicants how the Official Notice is applicable to, nor how it purports to support the rejection of Applicants' claim. Applicants recite a memory element, an on-board processor, and a switch that selectively switches the local memory bus to the system **memory** bus. Thus, Applicants are unable to understand, even if the assertion in the Office Action is correct, which Applicants do not concede, how the knowledge of storing a memory device in a memory card slot to interface to a system bus (in light of Figure 1 of the reference, this is understood by Applicants as referring to the main system bus on which the CPU resides, which is different from the main memory bus) would suggest to one skilled in the art to selectively switch a local memory bus from the on-board processor to a system memory bus via one of multiple system memory card slots. The issue is presently moot in light of the fact that Applicants have elected to expedite prosecution by incorporating elements of allowable claims into the base claim, however, Applicants submit that they would not be able to respond to rejection as it presently stands had they chosen a different course of action in response to the Office Action.

### **CONCLUSION**

For at least the foregoing reasons, Applicants submit that the rejections have been overcome, placing all claims in condition for allowance. Such action is earnestly solicited. The Examiner is respectfully requested to contact the undersigned by telephone if such contact would further the examination of the present application.

Please charge any shortages and credit any overcharges to our Deposit Account number 02-2666.

Respectfully submitted,  
**BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN, LLP**

Date: 7/12/05

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